



CAU 2811



- 1 -

Docket: 0756-1998

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Shunpei YAMAZAKI et al.
Serial No. 09/352,194
Filed: July 13, 1999
For: CRYSTALLINE SEMICONDUCTOR
THIN FILM, METHOD OF FABRICATING
THE SAME, SEMICONDUCTOR
DEVICE, AND METHOD OF FABRICATING
THE SAME

Group Art Unit: 2881

Examiner: F. ABRAHAM

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CERTIFICATE OF MAILING

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Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment and Response to Restriction Requirement in the above-identified application.

The fee has been calculated as shown below:

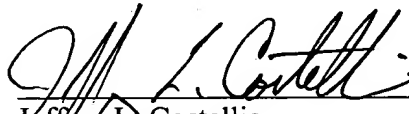
	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Small/Large Entity Rate	Fee
Total	23	Minus	20	3	x \$9/\$18	\$ 54.00
Indep.	8	Minus	8	-0-	x \$39/\$78	\$0.00
First Presentation of Multiple Dependent Claim					+ \$130/\$260	\$0.00
TOTAL						\$54.00

[X] A check in the amount of \$54.00 to cover the additional claims is enclosed.

[X] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.

- [X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



Jeffrey L. Costellia
Reg. No. 35,483

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EJR/JLC/wks

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Bka/00

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AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action of July 10, 2000, please amend the subject application as follows:

IN THE SPECIFICATION:

Page 30, line 16, change "15" to --VA--.

IN THE CLAIMS:

Cancel claims 1-4 without prejudice to applicants' to file a divisional application with respect thereto.

Please add the following new claims:

--19. A method of fabricating a semiconductor device including a thin film transistor, wherein the thin film transistor is formed through the steps of:

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